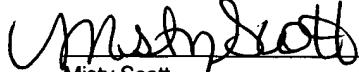




Serial No. 09/551,790

**PATENT**  
**IBM Docket No. RAL920000057US1**

I hereby certify that this correspondence (37 CFR 1.97(b)) and attached documents (i.e., PTO 1449 and copies of all cited references) are being deposited with the United States Postal Service as first class mail in an envelope with sufficient postage addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Misty Scott

MARCH 23, 2005  
Date of Deposit

**IN THE UNITED STATES PATENT & TRADEMARK OFFICE**

In re application of : Customer Number 25299  
W. G. Holland, et al. : IBM Corporation - 9CCA/B002  
: P.O. Box 12195  
Serial No. 09/551,790 : Research Triangle Park,  
: North Carolina 27709  
Filed: April 18, 2000 : Art Unit: 2188  
For: Computer System and Method with : Examiner: Jack A. Lane  
Internal Use of Network Switching :

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

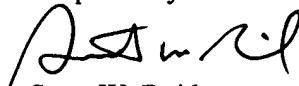
The Examiner's attention is directed to the enclosed references which were included in a Japanese Patent Office Action dated August 25, 2004 which relates to a Japanese counterpart application.

A form PTO-1449 listing the references and copies of the references are enclosed.

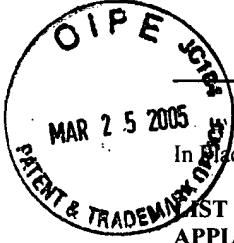
**PAYMENT OF FEE**

Please charge the IDS fee of **\$180.00** to IBM Corp. Deposit Account No. **09-1990**. The Commissioner is authorized to charge payment of any additional fees required under 37 CFR §1.16 and 37 CFR §1.17 or to credit any overpayment to the designated Deposit Account. A duplicate copy of this sheet is enclosed.

Respectfully submitted



Scott W. Reid  
Reg. No. 42,098, Attorney for Applicants  
Tel: 919-254-1085 / Fax: 919-254-2649  
Customer Number 25299



In Place of FORM PTO-1449 (Modified)

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANTS' INFORMATION DISCLOSURE  
STATEMENT**

Serial No.: 09/551,790  
Applicants: W. G. Holland, et al.  
Filing Date: August 18, 2000  
Group: 2188  
Atty. Docket No.: RAL920000057US1

Reference Designation

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes      No

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner Initial

bgA      VLIW Hardware Stack Processor, Kiyoshige Nakamura et al. Information Processing Society of Japan, Search Report, Vol.95. No. 80 (95-ARC-113) P.9- P16 / Publication date : 08/25/95

bgA      Programmable Engine Architecture for High-speed Network Processing, Hideyuki Shimonishi et al. The Institute of Electronics, Information and Communication Engineers, Technical Report of Ieice SSE99-127 PS99-51 (1999-12) ISSN: 0913-5685 Vol. 99 No. 507 P.31-P. 36 / Publication date: 12/17/99

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

\*\* Information Materials for IDS \*\*

To RAL IP Law Docket Number RAL9-2000-0057 Date Jan 31, 2005

Prepared by Etsuji Okada Date of JPO Office Action Aug 25, 2004

Applied Art (The following reference(s) were cited by JPO Examiner as Prior art to the following JP claims)

Ref.	Patent Document No. or Title	Publication Date (MM/DD/YY)	English abs. or counterpart document available (Y/N)	JP claim(s)
A	VLIW Hardware Stack Processor, Kiyohige Nakamura et al. Information Processing Society of Japan, Search Report, Vol. 95. No. 80 (95-ARC-113) P.9 ~ P.16	Aug. 25, 1995	Y	1-6
B				1-6
C	Programmable Engine Architecture for High-Speed Network Processing, Hideyuki Shimonishi et al.	Dec 17, 1999	Y	1-6
D	The Institute of Electronics, Information and Communication Engineers, Technical Report of Ieice S SE99-129.			
E	PS99-51 (1999-12) ISSN: 0913-5685 Vol 99 No 507 P.31 ~ P.36			
F				

<NOTE>

◇ PUPA : Published Unexamined Patent Application

◇ PUUMA : Published Unexamined Utility Model Application

◇ JP : Japanese Patent

to be continued

◇ PEPA : Published Examined Patent Application

◇ PEUMA : Published Examined Utility Model Application

◇ \* : Reference being filed before and published after the priority application date of the subject docket

Background Art (The following reference(s) were cited but not applied to the JP claims.)

Ref.	Patent Document No. or Title	Ref.	Patent Document No. or Title
bgA		bgD	
bgB		bgE	
bgC		bgF	

Comment, if any :

to be continued

EP COMPANY  
REPRESENTATIVE